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Sanjay Chaudhary, Gaurav Somani, Rajkumar Buyya

Distributed Computing Yoram Moses,2015-10-03 This book constitutes the proceedings of the 29th International Symposium on Distributed Computing, DISC 2015, held in Tokyo, Japan, in October 2015. The 42 full papers presented in this volume were carefully reviewed and selected from 143 submissions. The papers feature original contributions to theory, design, implementation, modeling, analysis, or application of distributed systems and networks. A number of 14 two-page brief announcements are included in the back matter of the proceedings.

Scene of the Cybercrime: Computer Forensics Handbook Syngress,2002-08-12 Cybercrime and cyber-terrorism represent a serious challenge to society as a whole. - Hans Christian Krüger, Deputy Secretary General of the Council of Europe Crime has been with us as long as laws have existed, and modern technology has given us a new type of criminal activity: cybercrime. Computer and network related crime is a problem that spans the globe, and unites those in two disparate fields: law enforcement and information technology. This book will help both IT pros and law enforcement specialists understand both their own roles and those of the other, and show why that understanding and an organized, cooperative effort is necessary to win the fight against this new type of crime. 62% of US companies reported computer-related security breaches resulting in damages of \$124 million dollars. This data is an indication of the massive need for Cybercrime training within the IT and law enforcement communities. The only book that covers Cybercrime from forensic investigation through prosecution. Cybercrime is one of the battlefields in the war against terror.

Information Security Elias Athanasopoulos, Bart Mennink, 2023-11-30 This book constitutes the proceedings of the 26th International Conference on Information Security, ISC 2023, which took place in Groningen, The Netherlands, in November 2023. The 29 full papers presented in this volume were carefully reviewed and selected from 90 submissions. The contributions were organized in topical sections as follows: privacy; intrusion detection and systems; machine learning; web security; mobile security and trusted execution; post-quantum cryptography; multiparty computation; symmetric cryptography; key management; functional and updatable encryption; and signatures, hashes, and cryptanalysis.

Transactional Memory. Foundations, Algorithms, Tools, and Applications Rachid Guerraoui, Paolo Romano, 2014-12-29 The advent of multi-core architectures and cloud-computing has brought parallel programming into the mainstream of software development. Unfortunately, writing scalable parallel programs using traditional lock-based synchronization primitives is well known to be a hard, time consuming and error-prone task, mastered by only a minority of specialized programmers. Building on the familiar abstraction of atomic transactions, Transactional Memory (TM) promises to free programmers from the complexity of conventional synchronization schemes, simplifying the development and verification of concurrent programs, enhancing code reliability, and boosting productivity. Over the last decade TM has been subject to intense research on a broad range of aspects including hardware and operating systems support, language integration, as well as algorithms and theoretical foundations. On the industrial side, the major players of the software and hardware markets have been up-front in the research and development of prototypal products providing support for TM systems. This has recently led to the introduction of hardware TM implementations on mainstream commercial microprocessors and to the integration of TM support for the world's leading open source compiler. In such a vast inter-disciplinary domain, the Euro-TM COST Action (IC1001) has served as a catalyzer and a bridge for the various research communities looking at disparate, yet subtly interconnected, aspects of TM. This book emerged from the idea having Euro-TM experts compile recent results in the TM area in a single and consistent volume. Contributions have been carefully selected and revised to provide a broad coverage of several fundamental issues associated with the design and implementation of TM systems, including their theoretical underpinnings and algorithmic foundations, programming language integration and verification tools, hardware supports, distrib

Transactional Memory Tim Harris, James R. Larus, Ravi Rajwar, 2010 The advent of multicore processors has renewed interest in the idea of incorporating transactions into the programming model used to write parallel programs. This approach, known as transactional memory, offers an alternative, and hopefully better, way to coordinate concurrent threads. The ACI (atomicity, consistency, isolation) properties of transactions provide a foundation to ensure that con-current reads and writes of shared data do not produce inconsistent or incorrect results. At a higher level, a computation wrapped in a transaction executes atomically---either it completes successfully and commits its result in its entirety or it aborts. In addition, isolation ensures the transaction produces the same result as if no other transactions were executing concurrently. Although transactions are not a parallel programming panacea, they shift much of the burden of synchronizing and co-ordinating parallel computations from a programmer to a compiler, to a language runtime system, or to hardware. The challenge for the system implementers is to build an efficient transactional memory infrastructure. This book presents an overview of the state of the art in the design and implementation of transactional memory systems, as of early spring 2010.

Transactional Memory, Second Edition Tim Harris, James Larus, Ravi Rajwar, 2022-05-31 The advent of multicore processors has renewed interest in the idea of incorporating transactions into the programming model used to write parallel programs. This approach, known as transactional memory, offers an alternative, and hopefully better, way to coordinate concurrent threads. The ACI (atomicity, consistency, isolation) properties of transactions provide a foundation to ensure that concurrent reads and writes of shared data do not produce inconsistent or incorrect results. At a higher level, a computation wrapped in a transaction executes atomically either it completes successfully and commits its result in its entirety or it aborts. In addition, isolation ensures the transaction produces the same result as if no other transactions were executing concurrently. Although transactions are not a parallel programming panacea, they shift much of the burden of synchronizing and coordinating parallel computations from a programmer to a compiler, to a language runtime system, or to hardware. The challenge for the system implementers is to build an efficient transactional memory infrastructure. This book presents an overview of the state of the art in the design and implementation of transactional memory systems, as of early spring 2010. Table of Contents: Introduction / Basic Transactions / Building on Basic Transactional Memory / Hardware-Supported Transactional Memory / Conclusions

Research Advances in Cloud Computing Sanjay Chaudhary, Gaurav Somani, Rajkumar Buyya, 2017-12-28 This book addresses the emerging area of cloud computing, providing a comprehensive overview of the research areas, recent work and open research problems. The move to cloud computing is no longer merely a topic of discussion; it has become a core competency that every modern business needs to embrace and excel at. It has changed the way enterprise and internet computing is viewed, and this success story is the result of the long-term efforts of computing research community around the globe. It is predicted that by 2026 more than two-thirds of all enterprises across the globe will be entirely run in cloud. These predictions have led to huge levels of funding for research and development in cloud computing and related technologies. Accordingly, universities across the globe have incorporated cloud computing and its related technologies in their curriculum, and information technology (IT) organizations are accelerating their skill-set evolution in order to be better prepared to manage emerging technologies and public expectations of the cloud, such as new services.

Indexing on Non-Volatile Memory Kaisong Huang, Tianzheng Wang, 2023-11-28 This book focuses on online transaction processing indexes designed for scalable, byte-addressable non-volatile memory (NVM) and provides a systematic review and summary of the fundamental principles and techniques as well as an outlook on the future of this research area. In this book, the authors divide the development of NVM indexes into three "eras"— pre-Optane, Optane and post-Optane—based on when the first major scalable NVM device (Optane) became commercially available and when it was announced to be discontinued. The book will analyze the reasons for the slow adoption of NVM and give an outlook for indexing techniques in the post-Optane era. The book assumes only basic undergraduate-level understanding on indexing (e.g., B+-trees, hash tables) and database systems in general. It is otherwise self-contained with the necessary background information, including an introduction to NVM hardware and software/programming issues, a detailed description of different indexes in highly concurrent systems for non-experts and new researchers to get started in this area.

Artificial Intelligence and Soft Computing, Part II Leszek Rutkowski,Rafal Scherer,Ryszard Tadeusiewicz,Lotfi A. Zadeh,Jacek M. Zurada,2010-06 This volume constitutes the proceedings of the 10th International Conference on Artificial Intelligence and Soft Computing, ICAISC'2010, held in Zakopane, Poland in June 13-17, 2010. The articles are organized in topical sections on Fuzzy Systems and Their Applications; Data Mining, Classification and Forecasting; Image and Speech Analysis; Bioinformatics and Medical Applications (Volume 6113) together with Neural Networks and Their Applications; Evolutionary Algorithms and Their Applications; Agent System, Robotics and Control; Various Problems aof Artificial Intelligence (Volume 6114).

Memory Carol A. Turkington,2003-02-06 * Master the 3 keys of memory * Boost memory power with self-tests * Remember everything better, from names and faces to articles and speeches Master key concepts.

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Euro-Par 2007 Workshops: Parallel Processing Luc Bougé, Franck Cappello, Jack J. Dongarra, Thomas Hérault, 2008

High Performance Computing Rio Yokota, Michèle Weiland, David Keyes, Carsten Trinitis, 2018-06-04 This book constitutes the refereed proceedings of the 33rd International Conference, ISC High Performance 2018, held in Frankfurt, Germany, in June 2018. The 20 revised full papers presented in this book were carefully reviewed and selected from 81 submissions. The papers cover the following topics: Resource Management and Energy Efficiency; Performance Analysis and Tools; Exascale Networks; Parallel Algorithms.

The Complete PC Upgrade and Maintenance Guide Mark Minasi,2004-03-09 The World's Leading PC Guide--Updated, Expanded, Reorganized The Complete PC Upgrade and Maintenance Guide continues its reign as the PC world's great problem-solving wonder. In simple, easy-to-follow language, it shows you how to prevent disasters, fix the ones that occur, and maximize your PC's power and longevity. Based on author Mark Minasi's popular seminars, this book is an unbeatable value. It teaches you everything you need to know to meet all your PC challenges, present and future. Coverage includes: * Preventing hardware disasters * Upgrading memory * Replacing power supplies * Installing EIDE and Serial ATA hard drives * Adding SCSI ports * Partitioning drives using NTFS and FAT32 * Protecting your PC from viruses, worms, and spyware * Solving data backup challenges * Adding and repairing DVDs and CD-ROMs * Installing and troubleshooting scanners * Troubleshooting printers * Installing communications devices * Resolving mouse and keyboard problems * Installing video and sound cards * Tackling networking issues * Installing a wireless network * Troubleshooting Internet connectivity * Solving laptop problems

Macquarie Guide: HSC Information Processes & Technology George Stamell,2007-11-10 Macquarie Revision Guides is a series of study aids written and recommended by teachers in NSW. Each guide presents a clear and up-to-date review of coursework and skills needed to do well in exams. Students, tutors, teachers and parents will find the practical approach of this series an essential support to the competitive final years of school study.

Historical Memory and Foreign Policy Lina Klymenko, Marco Siddi, 2022-10-13 This book explores the uses of the past in foreign policy-making. It outlines why and how political leaders refer to historical events in contemporary foreign policy discourses; the goals they hope to achieve; and the sometimes unintended foreign policy consequences of their (ab)use of historical memory. Furthermore, it looks at how political leaders shape domestic collective memories in pursuit of their international agendas, and highlight historical events leaders forget, reinterpret or obscure through selective narratives. The chapters explore a variety of theoretical concepts that shed light on how memory and foreign policy are linked in a complex and reciprocal way. The following mechanisms are discussed: the application of historical analogies; the construction of historical narratives; the creation of memory sites; the marginalisation and forgetting of the past; and the securitisation of historical memory. Through the use of a number of methodological approaches (such as discourse analysis, narrative analysis and content analysis of securitising moves) and a broad range of qualitative and quantitative data (newspaper articles, policy documents, commemorative speeches, interviews with policymakers and the observation of memory sites), the contributions highlight the interdependence of the international, regional and local dimensions of memory practices and history writing. Although they mostly focus on national case studies of foreign policy-making, they also reveal how representations of historical events evolve through interaction between political actors at the international level of analysis. The collection originated in the section entitled 'Exploring the Link between Historical Memory and Foreign Policy' at the annual Pan-European Conference of the European International Studies Association (EISA) 2018 held in Prague, the Czech Republic.

Fear Memory Integration Jim Pullaro,2005-11 Neurosis is not a medical condition. Rather, it is a chronic functional disorder of the autonomic nervous system resulting from environmental conditioning, which begins in the womb. It is caused by the imprinting of, and the continuous retrieval and repression of, fear memories. This imbalance interferes with the nervous system's ability to correctly respond to present sensory input and precipitates toxicosis of the autonomic nervous system. The nervous system's attempts to detoxify itself results in ongoing exaggerated autonomic responses such as rage, anxiety, and depression. Neurosis can be healed by intentional fear memory retrieval, in portions that are small enough to be holistically integrated. This process reverses the condition of neural toxicosis, thereby relieving excessive mood swings. Neurotic tension, and the act out that is driven by this tension, is also decreased in direct proportion to the amount of fear memory that is processed. All of the symptoms that are presently being catalogued and grouped into separate and overlapping categories of mental illness, are actually each person's unique, learned responses to their automatic fear memory recall. The neurotic act out is a symptom, which is a projection of the constantly occurring fear response onto a present situation.

Non-Volatile Memory Database Management Systems Joy Arulraj, Andrew Pavlo, 2022-06-01 This book explores the implications of non-volatile memory (NVM) for database management systems (DBMSs). The advent of NVM will fundamentally change the dichotomy between volatile memory and durable storage in DBMSs. These new NVM devices are almost as fast as volatile memory, but all writes to them are persistent even after power loss. Existing DBMSs are unable to take full advantage of this technology because their internal architectures are predicated on the assumption that memory is volatile. With NVM, many of the components of legacy

DBMSs are unnecessary and will degrade the performance of data-intensive applications. We present the design and implementation of DBMS architectures that are explicitly tailored for NVM. The book focuses on three aspects of a DBMS: (1) logging and recovery, (2) storage and buffer management, and (3) indexing. First, we present a logging and recovery protocol that enables the DBMS to support near-instantaneous recovery. Second, we propose a storage engine architecture and buffer management policy that leverages the durability and byte-addressability properties of NVM to reduce data duplication and data migration. Third, the book presents the design of a range index tailored for NVM that is latch-free yet simple to implement. All together, the work described in this book illustrates that rethinking the fundamental algorithms and data structures employed in a DBMS for NVM improves performance and availability, reduces operational cost, and simplifies software development.

OpenMP: Memory, Devices, and Tasks Naoya Maruyama, Bronis R. de Supinski, Mohamed Wahib, 2016-09-28 This book constitutes the proceedings of the 12th International Workshop on OpenMP, IWOMP 2016, held in Nara, Japan, in October 2016. The 24 full papers presented in this volume were carefully reviewed and selected from 28 submissions. They were organized in topical sections named: applications, locality, task parallelism, extensions, tools, accelerator programming, and performance evaluations and optimization.

Unconscious Memory Representations in Perception István Czigler,István Winkler,2010 Perceptual experience emerges from neural computations. Unconscious Memory Representations in Perception focuses on the role of implicit (non-conscious) memories in processing sensory information. Making sense of the wealth of information arriving at our senses requires implicit memories, which represent environmental regularities, contingencies of the sensory input, as well as general contextual knowledge. Recent findings and theories in cognitive and computational neuroscience provided new insights into the structure and contents of implicit memory representations. The chapters of this book examine implicit memories both in relatively simple situations, such as perceiving auditory and visual objects, as well as in high?level cognitive functions, such as speech and music perception and aesthetic experience. By nature, implicit memories cannot be directly studied with behavioral methods. Therefore, a large part of the evidence reviewed was obtained in neuroscientific studies. Readers with limited experience in neuroscience will find information about the most commonly used techniques in the appendix of this volume. (Series B)

Mem-elements for Neuromorphic Circuits with Artificial Intelligence Applications Christos Volos, Viet-Thanh Pham, 2021-06-17 Mem-elements for Neuromorphic Circuits with Artificial Intelligence Applications illustrates recent advances in the field of mem-elements (memristor, memcapacitor, meminductor) and their applications in nonlinear dynamical systems, computer science, analog and digital systems, and in neuromorphic circuits and artificial intelligence. The book is mainly devoted to recent results, critical aspects and perspectives of ongoing research on relevant topics, all involving networks of mem-elements devices in diverse applications. Sections contribute to the discussion of memristive materials and transport mechanisms, presenting various types of physical structures that can be fabricated to realize mem-elements in integrated circuits and device modeling. As the last decade has seen an increasing interest in recent advances in mem-elements and their applications in neuromorphic circuits and artificial intelligence, this book will attract researchers in various fields. Covers a broad range of interdisciplinary topics between mathematics, circuits, realizations, and practical applications related to nonlinear dynamical systems, nanotechnology, analog and digital systems, computer science and artificial intelligence Presents recent advances in the field of mem-elements (memristor, memcapacitor, meminductor) Includes interesting applications of mem-elements in nonlinear dynamical systems, analog and digital systems, neuromorphic circuits, computer science and artificial intelligence

Ram Upgrade.htm Book Review: Unveiling the Power of Words

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