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Roman Wölfel

**Software Development for Embedded Multi-core Systems** Max Domeika, 2011-04-08 The multicore revolution has reached the deployment stage in embedded systems ranging from small ultramobile devices to large telecommunication servers. The transition from single to multicore processors, motivated by the need to increase performance while conserving power, has placed great responsibility on the shoulders of software engineers. In this new embedded multicore era, the toughest task is the development of code to support more sophisticated systems. This book provides embedded engineers with solid grounding in the skills required to develop software targeting multicore processors. Within the text, the author undertakes an in-depth exploration of performance analysis, and a close-up look at the tools of the trade. Both general multicore design principles and processor-specific optimization techniques are revealed. Detailed coverage of critical issues for multicore employment within embedded systems is provided, including the Threading Development Cycle, with discussions of analysis, design, development, debugging, and performance tuning of threaded applications. Software development techniques engendering optimal mobility and energy efficiency are highlighted through multiple case studies, which provide practical "how-to" advice on implementing the latest multicore processors. Finally, future trends are discussed, including terascale, speculative multithreading, transactional memory, interconnects, and the software-specific implications of these looming architectural developments. Table of Contents Chapter 1 - Introduction Chapter 2 - Basic System and Processor Architecture Chapter 3 - Multi-core Processors & Embedded Chapter 4 - Moving To Multi-core Intel Architecture Chapter 5 - Scalar Optimization & Usability Chapter 6 - Parallel Optimization Using Threads Chapter 7 - Case Study: Data Decomposition Chapter 8 - Case Study: Functional Decomposition Chapter 9 - Virtualization & Partitioning Chapter 10 - Getting Ready For Low Power Intel Architecture Chapter 11 - Summary, Trends, and Conclusions Appendix I Glossary References \*This is the only book to explain software optimization for embedded multi-core systems \*Helpful tips, tricks and design secrets from an Intel programming expert, with detailed examples using the popular X86 architecture \*Covers hot topics, including ultramobile devices, low-power designs, Pthreads vs. OpenMP, and heterogeneous cores

**Server+ Certification** Elton Jernigan, 2002 If you're a candidate for Server+ certification, which measures essential competencies in advanced PC hardware issues such as RAID, SCSI, multiple CPUs, SANs, and much more, the Training Guide has what you need to pass. We have partnered with Elton Jernigan, a Subject Matter Expert (SME) of the initial Focus Group for development of the Server+ exam. He brings you an excellent resource that not only will help you pass the exam, but will also prove to be a handy, concise reference for managers and technicians who must select and implement hardware for network servers. You will benefit from Elton's insight as a 27-year veteran of the IT industry, including his experience as Director of Technology for the College of Business at Florida State University and as a senior computer trainer for the Beacon Institute for Learning. We make the most of your Server+ Certification study time by providing: Content that is organized according to each job dimension and exam objective Exam objectives that are clearly detailed and explained Study strategies to optimize your learning Exam tips that provide specific exam-related advice Step-by-step instructions that walk you through a task and help you learn faster Additional content sections with in-depth reference material Chapter summaries that review key concepts Key terms you'll need to understand Resource URLs that list web sites you can access for additional information on topics in each chapter Exercises that provide concrete experiences to reinforce learning Review questions and answers to assess your comprehension Sample exam questions that include answers and detailed explanations

**Assembly Language for Intel-based Computers** Kip R. Irvine, 2003

**Closing the Power Gap between ASIC & Custom** David Chinnery, Kurt Keutzer, 2008-01-23 Explains how to use low power design in an automated design flow, and examine the design time and performance trade-offs Includes the latest tools and techniques for low power design applied in an ASIC design flow Focuses on low power in an automated design methodology, a much neglected area

**Advanced Design and Implementation of Virtual Machines** Xiao-Feng Li, 2016-12-19 Along with the increasingly important runtime engines pervasive in our daily-life computing, there is a strong demand from the software community for a solid presentation on the design and implementation of modern virtual machines, including the Java virtual machine, JavaScript engine and Android execution engine. The community expects to see not only formal algorithm description, but also pragmatic code snippets; to understand not only research topics, but also engineering solutions. This book meets these demands by providing a unique description that combines high level design with low level implementations and academic advanced topics with commercial solutions. This book takes a holistic approach to the design of VM architecture, with contents organized into a consistent framework, introducing topics and algorithms in an easily understood step by step process. It focuses on the critical aspects of VM design, which are often overlooked in other works, such as runtime helpers, stack unwinding and native interface. The algorithms are fully illustrated in figures and implemented in easy to digest code snippets, making the abstract concepts tangible and programmable for system software developers.

**Power-efficient System Design** Preeti Ranjan Panda, B. V. N. Silpa, Aviral Shrivastava, Krishnaiah Gummidipudi, 2010-07-23 The Information and communication technology (ICT) industry is said to account for 2% of the worldwide carbon emissions – a fraction that continues to grow with the relentless push for more and more sophisticated computing equipment, communications infrastructure, and mobile devices. While computers evolved in the direction of higher and higher performance for most of the latter half of the 20th century, the late 1990's and early 2000's saw a new emerging fundamental concern that has begun to shape our day-to-day thinking in system design – power dissipation. As we elaborate in Chapter 1, a variety of factors colluded to raise power efficiency as a first class design concern in the designer's mind, with profound consequences all over the field: semiconductor process design, circuit design, design automation tools, system and application software, all the way to large data centers. Power-efficient System Design originated from a desire to capture and highlight the exciting developments in the rapidly evolving field of power and energy optimization in electronic and computer based systems. Tremendous progress has been made in the last two decades, and the topic continues to be a fascinating research area. To develop a clearer focus, we have concentrated on the relatively higher level of design abstraction that is loosely called the system level. In addition to the extensive coverage of traditional power reduction targets such as CPU and memory, the book is distinguished by detailed coverage of relatively modern power optimization ideas focussing on components such as compilers, operating systems, servers, data centers, and graphics processors.

**Stabilization, Safety, and Security of Distributed Systems** Toshimitsu Masuzawa, Sébastien Tixeuil, 2007-11-06 This book constitutes the refereed proceedings of the 9th International Symposium on Stabilization, Safety, and Security of Distributed Systems, SSS 2007, held in Paris, France, November 14-16, 2007. The 27 regular papers presented together with the extended abstracts of three invited lectures were carefully reviewed and selected from 64 submissions. The papers address all aspects of self-stabilization, safety and security, recovery oriented systems and programming.

**Digital Design and Fabrication** Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry, this volume is organized into five, information-rich sections. Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them. Featuring contributions from leading experts, the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies. Developing advanced concepts, this sharply focused book— Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits, whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state-of-the-art advances in fabrication and CMOS technologies Describes performance evaluation measures, which are the bottom line from the user's point of view Discusses design techniques used to create modern computer systems, including high-speed computer arithmetic and high-frequency design, timing and clocking, and PLL and DLL design

**The Computer Engineering Handbook** Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering. The unparalleled rate of technological advancement, the explosion of computer applications, and the now-in-progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own

**InfoWorld**, 2003-07-14 InfoWorld is targeted to Senior IT professionals. Content is segmented into Channels and Topic Centers. InfoWorld also celebrates people, companies, and projects.

*Euro-Par 2014: Parallel Processing* Fernando Silva, Inês Dutra, Vitor Santos Costa, 2014-08-11 This book constitutes the refereed proceedings of the 20th International Conference on Parallel and Distributed Computing, Euro-Par 2014, held in Porto, Portugal, in August 2014. The 68 revised full papers presented were carefully reviewed and selected from 267 submissions. The papers are organized in 15 topical sections: support tools environments; performance prediction and evaluation; scheduling and load balancing; high-performance architectures and compilers; parallel and distributed data management; grid, cluster and cloud computing; green high performance computing; distributed systems and algorithms; parallel and distributed programming; parallel numerical algorithms; multicore and manycore programming; theory and algorithms for parallel computation; high performance networks and communication; high performance and scientific applications; and GPU and accelerator computing.

**CIO**, 2003-07-15

*Maoist Insurgency and India's Internal Security Architecture* E M Rammohun, Amritpal Singh, A K Agarwal, 2012-08-24 The Maoists have taken up arms. Their focus is on tribals and lower caste people for support. Stress is on militarisation with hierarchy and building of 'People's Guerrilla Army' capable of destroying the state machinery. Violence and breakdown of law and order is causing loss of innocent lives and damage to property. The book has been covered in five chapters by the contributors. The first chapter deals with the Maoist insurgency in India and analyses the reasons that led to the Communist Party of India (CPI) taking up the cause of the lower castes and scheduled tribes. The second chapter deals with use of air power in combating the Maoist insurgency. The author has suggested deployment of drones to detect insurgent camps in the forests and use of helicopters for evacuation of casualties and other measures to facilitate logistics support for Countering Insurgency. An appraisal of India's Intelligence agencies has been covered in Chapter Three. The fourth and fifth chapters are concerning the manner in which the United States has reacted after the 9/11 terrorist attack in the USA and how India reacted after the terrorist attack in Mumbai on 26 November 2008. The author has suggested the need to evolve a Comprehensive Internal Security Policy covering all dimensions and all levels -- political, economic and social.

A+ Charles J. Brooks, 2003 bull; Updated edition of best-selling book (100,000 copies sold!) written by Charles J. Brooks of CompTIA's A+ Advisory Council. bull; Features Marcraft's Dynamic Test Tracking system - chapters, labs, and review questions updated online so the book is never out of date! bull; Exclusive voucher for 30% off on each exam, a \$75 savings!

**Computer Structure and Logic** Dave Prowse, Mark Edward Soper, Scott Mueller, 2011-01-25 Computer Structure and Logic Pearson Certification Team The place to start your computer career! Learn about computers and networks from the ground up! Learn about computers and networks from the ground up! Your first step toward certifications from CompTIA, Microsoft, or Cisco... absolutely no experience necessary! Explains every part of your computer and shows how each part works together Teaches simple troubleshooting and repair techniques Packed with real-world examples and case studies Master the basics and build your strong foundation for success! I/O: How information gets into and out of computers Motherboards and buses: How your computer's parts are connected CPU: How your computer's "brain" works—and how to install and troubleshoot it Memory and storage: The types you need and how to install them Bootup: How your computer starts, what can go wrong, and how to fix it Operating systems: The basics of Windows, Mac OS X, and Linux Basic security: Protecting your data, connections, and computer Troubleshooting: The tools and methods every good PC technician must know Networks and the Internet: How they work, how they communicate, and how to connect to them Test your knowledge, gain confidence, and succeed! More than 150 questions, with clear explanations of every answer!

**Upgrading and Repairing PCs** Scott Mueller, 2011-08-11 "...a comprehensive resource for PC enthusiasts and professionals alike. Packed with the latest speeds and feeds, you'll want to keep this book on-hand as an authoritative technology reference." –Chris Angelini, Managing Editor, Tom's Hardware For 20 years, Upgrading and Repairing PCs has been the world's #1 guide to PC hardware: the single source for reliable information on troubleshooting and fixing problems, adding hardware, optimizing performance, and building new PCs. Now, better than ever, this 20th Edition offers beefed-up coverage of the newest hardware innovations and maintenance techniques, plus more than 90 minutes of new DVD video. Scott Mueller delivers practical answers about PC processors, motherboards, buses, BIOSes, memory, storage, video, audio, I/O, input devices, networks, Internet connectivity, power, and much more. You'll find the industry's best coverage of diagnostics, testing, and repair—plus cutting-edge discussions of improving performance via overclocking and other techniques. Mueller has taught thousands of professionals in person and millions more through his books and videos—nobody knows more about keeping PCs running perfectly. Whether you're a professional technician, a small business owner trying to save money, or a home PC enthusiast, this is the only PC hardware book you need! NEW IN THIS EDITION The newest processors, including Intel's 2nd generation Core i3, i5, i7 plus the Atom, and AMD's new VISION series CPUs 3TB (and larger) disks, 4K sectoring, partition alignment, faster SATA disk interfaces, and SSD (Solid State Drive) hard drive replacements New firmware innovations, from full UEFI BIOS support to built-in motherboard flash BIOS upgrade utilities Integrated video and audio, including 5.1/7.1 surround sound, HDMI, and DisplayPort connections Updated PCI Express and Power Supply specifications for powering high-end video cards Emerging interfaces such as USB 3.0 and Thunderbolt Updated coverage of building PCs from scratch—from choosing and assembling hardware through BIOS setup and troubleshooting ON THE DVD Get more than 90 minutes of up-to-the minute, studio quality how-to videos—all playable on your DVD player or computer! In this edition, Scott Mueller offers true insider information about several of the key components in a PC, including hard disk drives, power supplies, motherboards, and more. You will see hard drives completely dissected—even see an open hard drive in operation—so you can observe exactly what happens under the covers. One of the most common causes of failure in modern motherboards, power supplies, and many other PC components are bad capacitors—otherwise known as the capacitor plague. Mueller will show several real-world examples of this "disease," so you can easily identify and perhaps even repair the problem. Finally, because external hard drives are now commonly used for supplementary storage and backups, you'll find a complete discussion of several alternatives on the market. This includes tips and tricks for building or assembling your own flexible, high performance and highly reliable external storage drives, with several examples shown. This DVD also contains the complete 19th edition of this book in printable form, plus extensive technical reference material, a comprehensive glossary, and more!

**Mastering Microsoft Virtualization** Tim Cerling, Jeffrey L. Buller, 2011-03-04 The first in-depth, comprehensive guide to Microsoft's suite of virtualization products Virtualization is a hot topic for IT because of the potential it offers for serious economic benefits. While other books treat server virtualization alone, this comprehensive guide provides a complete virtual strategy. You will learn how to deploy a complete virtualization stack with Microsoft's offerings in server virtualization, application virtualization, presentation virtualization, and desktop virtualization. Written by Microsoft technology product specialists, this guide provides real-world focus, enabling you to create a complete IT system that is highly efficient and cost effective. Covers Windows Server 2008 Hyper-V 2.0, Remote Desktop Services, Microsoft Application Virtualization (App-V), Virtual Desktop Infrastructure (VDI), and Microsoft Enterprise Desktop Virtualization (MED-V) Demonstrates how to deploy a virtual infrastructure—from the server to the desktop Goes beyond any other book on Microsoft virtualization Covers the highly anticipated new feature Live Migration This guide, part of the popular Sybex Mastering series, offers every IT administrator a road map for implementing an efficient and successful virtualization project.

*Intel Threading Building Blocks* James Reinders, 2007-07-12 Multi-core chips from Intel and AMD offer a dramatic boost in speed and responsiveness, and plenty of opportunities for multiprocessing on ordinary desktop computers. But they also present a challenge: More than ever, multithreading is a requirement for good performance. This guide explains how to maximize the benefits of these processors through a portable C++ library that works on Windows, Linux, Macintosh, and Unix systems. With it, you'll learn how to use Intel Threading Building Blocks (TBB) effectively for parallel programming -- without having to be a threading expert. Written by James Reinders, Chief Evangelist of Intel Software Products, and based on the experience of Intel's developers and customers, this book explains the key tasks in multithreading and how to accomplish them with TBB in a portable and robust manner. With plenty of examples and full reference material, the book lays out common patterns of uses, reveals the gotchas in TBB, and gives important guidelines for choosing among alternatives in order to get the best performance. You'll learn how Intel Threading Building Blocks: Enables you to specify tasks instead of threads for better portability, easier programming, more understandable source code, and better performance and scalability in general Focuses on the goal of parallelizing computationally intensive work to deliver high-level solutions Is compatible with other threading packages, and doesn't force you to pick one package for your entire program Emphasizes scalable, data-parallel programming,

which allows program performance to increase as you add processors Relies on generic programming, which enables you to write the best possible algorithms with the fewest constraints Any C++ programmer who wants to write an application to run on a multi-core system will benefit from this book. TBB is also very approachable for a C programmer or a C++ programmer without much experience with templates. Best of all, you don't need experience with parallel programming or multi-core processors to use this book.

**JAVA Developer's Journal** ,2003

**Empirical Studies in Applied Economics** Jeffrey A. Dubin,2012-12-06 Empirical Studies In Applied Economics presents nine previously unpublished analyses in monograph form. In this work, the topics are presented so that each chapter stands on its own. The emphasis is on the applications but attention is also given to the econometric and statistical issues for advanced readers. Econometric methods include multivariate regression analysis, limited dependent variable analysis, and other maximum likelihood techniques. The empirical topics include the measurement of competition and market power in natural gas transportation markets and in the pharmaceutical market for chemotherapy drugs. Additional topics include an empirical analysis of NFL football demand, the accuracy of an econometric model for mail demand, and the allocation of police services in rural Alaska. Other chapters consider the valuation of technology patents and the determination of patent scope, duration, and reasonable royalty, and the reaction of financial markets to health scares in the fast-food industry. Finally, two chapters are devoted to the theory and testing of synergistic health effects from the combined exposure to asbestos and cigarette smoking.

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