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Anuradha D. Thakare,Sheetal Umesh Bhandari

High-Performance IT Services Terry Critchley,2016-10-04 This book on performance fundamentals covers UNIX, OpenVMS, Linux, Windows, and MVS. Most of the theory and systems design principles can be applied to other operating systems, as can some of the benchmarks. The book equips professionals with the ability to assess performance characteristics in unfamiliar environments. It is suitable for practitioners, especially those whose responsibilities include performance management, tuning, and capacity planning. IT managers with a technical outlook also benefit from the book as well as consultants and students in the world of systems for the first time in a professional capacity.

Distributed Computing Yoram Moses,2015-10-03 This book constitutes the proceedings of the 29th International Symposium on Distributed Computing, DISC 2015, held in Tokyo, Japan, in October 2015. The 42 full papers presented in this volume were carefully reviewed and selected from 143 submissions. The papers feature original contributions to theory, design, implementation, modeling, analysis, or application of distributed systems and networks. A number of 14 two-page brief announcements are included in the back matter of the proceedings.

Workplace-Based Assessments in Psychiatry Amit Malik,Dinesh Bhugra,Andrew Brittlebank,2011-06 This book outlines the workplace-based assessments (WPBAs) that are required by the current competency-based psychiatry curriculum. The authors explore the theory and practice of different assessment methods such as case-based discussion, long-case evaluation and directly observed practice, changes in the MRCPsych examinations and multi-source feedback.

Artificial Intelligence Applications and Reconfigurable Architectures Anuradha D. Thakare,Sheetal Umesh Bhandari,2023-03-21 ARTIFICIAL INTELLIGENCE APPLICATIONS and RECONFIGURABLE ARCHITECTURES The primary goal of this book is to present the design, implementation, and performance issues of AI applications and the suitability of the FPGA platform. This book covers the features of modern Field Programmable Gate Arrays (FPGA) devices, design techniques, and successful implementations pertaining to AI applications. It describes various hardware options available for AI applications, key advantages of FPGAs, and contemporary FPGA ICs with software support. The focus is on exploiting parallelism offered by FPGA to meet heavy computation requirements of AI as complete hardware implementation or customized hardware accelerators. This is a comprehensive textbook on the subject covering a broad array of topics like technological platforms for the implementation of AI, capabilities of FPGA, suppliers' software tools and hardware boards, and discussion of implementations done by researchers to encourage the AI community to use and experiment with FPGA. Readers will benefit from reading this book because It serves all levels of students and researcher's as it deals with the basics and minute details of Ecosystem Development Requirements for Intelligent applications with reconfigurable architectures whereas current competitors' books are more suitable for understanding only reconfigurable architectures. It focuses on all aspects of machine learning accelerators for the design and development of intelligent applications and not on a single perspective such as only on reconfigurable architectures for IoT applications. It is the best solution for researchers to understand how to design and develop various AI, deep learning, and machine learning applications on the FPGA platform. It is the best solution for all types of learners to get complete knowledge of why reconfigurable architectures are important for implementing AI-ML applications with heavy computations. Audience Researchers, industrial experts, scientists, and postgraduate students who are working in the fields of computer engineering, electronics, and electrical engineering, especially those specializing in VLSI and embedded systems, FPGA, artificial intelligence, Internet of Things, and related multidisciplinary projects.

The Elements of Creativity and Giftedness in Mathematics B. Sriraman,Kyeong Hwa Lee,2011-07-23 The Elements of Creativity and Giftedness in Mathematics edited by Bharath Sriraman and KyeongHwa Lee covers recent advances in mathematics education pertaining to the development of creativity and giftedness. The book is international in scope in the "sense" that it includes numerous studies on mathematical creativity and giftedness conducted in the U.S.A, China, Korea, Turkey, Israel, Sweden, and Norway in addition to cross-national perspectives from Canada and Russia. The topics include problem -posing, problem-solving and mathematical creativity; the development of mathematical creativity with students, pre and in-service teachers; cross-cultural views of creativity and giftedness; the unpacking of notions and labels such as high achieving, inclusion, and potential; as well as the theoretical state of the art on the constructs of mathematical creativity and giftedness. The book also includes some contributions from the first joint meeting of the American Mathematical Society and the Korean Mathematical Society in Seoul, 2009. Topics covered in the book are essential reading for graduate students and researchers interested in researching issues and topics within the domain of mathematical creativity and mathematical giftedness. It is also accessible to pre-service and practicing teachers interested in developing creativity in their classrooms, in addition to professional development specialists, mathematics educators, gifted educators, and psychologists.

End-to-end Qos Network Design Tim Szigeti,Christina Hattingh,2005 Best-practice QoS designs for protecting voice, video, and critical data while mitigating network denial-of-service attacks Understand the service-level requirements of voice, video, and data applications Examine strategic QoS best practices, including Scavenger-class QoS tactics for DoS/worm mitigation Learn about QoS tools and the various interdependencies and caveats of these tools that can impact design considerations Learn how to protect voice, video, and data traffic using various QoS mechanisms Evaluate design recommendations for protecting voice, video, and multiple classes of data while mitigating DoS/worm attacks for the following network infrastructure architectures: campus LAN, private WAN, MPLS VPN, and IPSec VPN Quality of Service (QoS) has already proven itself as the enabling technology for the convergence of voice, video, and data networks. As business needs evolve, so do the demands for QoS. The need to protect critical applications via QoS mechanisms in business networks has escalated over the past few years, primarily due to the increased frequency and sophistication of denial-of-service (DoS) and worm attacks. End-to-End QoS Network Design is a detailed handbook for planning and deploying QoS solutions to address current business needs. This book goes beyond discussing available QoS technologies and considers detailed design examples that illustrate where, when, and how to deploy various QoS features to provide validated and tested solutions for voice, video, and critical data over the LAN, WAN, and VPN. The book starts with a brief background of network infrastructure evolution and the subsequent need for QoS. It then goes on to cover the various QoS features and tools currently available and comments on their evolution and direction. The QoS requirements of voice, interactive and streaming video, and multiple classes of data applications are presented, along with an overview of the nature and effects of various types of DoS and worm attacks. QoS best-practice design principles are introduced to show how

QoS mechanisms can be strategically deployed end-to-end to address application requirements while mitigating network attacks. The next section focuses on how these strategic design principles are applied to campus LAN QoS design. Considerations and detailed design recommendations specific to the access, distribution, and core layers of an enterprise campus network are presented. Private WAN QoS design is discussed in the following section, where WAN-specific considerations and detailed QoS designs are presented for leased-lines, Frame Relay, ATM, ATM-to-FR Service Interworking, and ISDN networks. Branch-specific designs include Cisco® SAFE recommendations for using Network-Based Application Recognition (NBAR) for known-worm identification and policing. The final section covers Layer 3 VPN QoS design-for both MPLS and IPSec VPNs. As businesses are migrating to VPNs to meet their wide-area networking needs at lower costs, considerations specific to these topologies are required to be reflected in their customer-edge QoS designs. MPLS VPN QoS design is examined from both the enterprise and service provider's perspectives. Additionally, IPSec VPN QoS designs cover site-to-site and teleworker contexts. Whether you are looking for an introduction to QoS principles and practices or a QoS planning and deployment guide, this book provides you with the expert advice you need to design and implement comprehensive QoS solutions.

  InfoWorld ,2003-06-16 InfoWorld is targeted to Senior IT professionals. Content is segmented into Channels and Topic Centers. InfoWorld also celebrates people, companies, and projects.

  Universities and Research Armando Jorge Lopes,1998

  Contemporary High Performance Computing Jeffrey S. Vetter,2013-04-23 Contemporary High Performance Computing: From Petascale toward Exascale focuses on the ecosystems surrounding the world's leading centers for high performance computing (HPC). It covers many of the important factors involved in each ecosystem: computer architectures, software, applications, facilities, and sponsors. The first part of the book examines significant trends in HPC systems, including computer architectures, applications, performance, and software. It discusses the growth from terascale to petascale computing and the influence of the TOP500 and Green500 lists. The second part of the book provides a comprehensive overview of 18 HPC ecosystems from around the world. Each chapter in this section describes programmatic motivation for HPC and their important applications; a flagship HPC system overview covering computer architecture, system software, programming systems, storage, visualization, and analytics support; and an overview of their data center/facility. The last part of the book addresses the role of clouds and grids in HPC, including chapters on the Magellan, FutureGrid, and LLGrid projects. With contributions from top researchers directly involved in designing, deploying, and using these supercomputing systems, this book captures a global picture of the state of the art in HPC.

  Virtual Teams Jessica Lipnack,Jeffrey Stamps,2008-09-22 Praise for the First Edition of Virtual Teams If you want to see where organizational communications are going in the future, heed what these pioneers have written today. —Howard Rheingold, author, The Virtual Community, and founder, Electric Mind Lipnack and Stamps have written an important book for the twenty-first-century corporation. —Regis McKenna, The McKenna Group, author, Relationship Marketing This book provides a long overdue perspective on how to apply the discipline of real teams in the fast-moving, increasingly dispersed information age of the future. —Jon R. Katzenbach, author, The Wisdom of Teams For those who want to lead the movement, catch up with it, or simply know where it is going, this book is packed with useful information and interesting stories. —Dee W. Hock, founder and chairman emeritus, VISA Virtual Teams provides valuable insights into global teamwork and management through network technologies now available to all companies, large or small. —Jim Lynch, director, corporate quality, Sun Microsystems, Inc.

  Digital Transformation Of Property In Greater China, The: Finance, 5g, Ai, And Blockchain Paul Schulte,Dean Sun,Roman Y Shemakov,2021-05-05 'A masterful narration on the digitization of property in China.'Tan YinglanFounding Managing PartnerInsignia Ventures Partners, Singapore'...captures the fascinating story of 'smart city initiatives' and tells you all you need to know.'Ben ShenglinProfessor & DeanInternational Business SchoolZhejiang University, Hangzhou'...smartly combines economics, geo-politics, finance and real estate.'Joshua VargheseFounding Partner, Axia Real Assets, TorontoLong-planned advances in China — in 5G, blockchain, central bank coins, and SME superapps — have coalesced into a new world of digitized, tokenized, and tradable assets. New digital mega-projects like the Blockchain Service Network, smart cities, and new foreign exchange digital rails are animating physical assets: offices, warehouses, homes, and farms. Powered by a network of sensors, AI, and distributed trust, property has digitized wings. The resulting inflow of data from every part of the 'built' world will create new industries, uproot traditional finance, and transform cities.The global trade war is not just a re-ordering of technology: it's a re-ordering of cities. Nations which export this digital technology first will alter the digital fabric of the developing world. A digital Non-Aligned Movement is afoot! One way for the US to catch up is public-private partnerships between Silicon Valley and DC — or just 'copy' China. This book explores the many people and companies, large and small, which are blazing new trails in China's 'Internet of Everything' to transform the way we live, buy, and move.

  Wiley Encyclopedia of Telecommunications ,2003

  Wiley Encyclopedia of Telecommunications, Volume 2 John G. Proakis,2003 Contains 275 tutorial articles focused on modern telecommunications topics. The contents include articles on communication networks, source coding and decoding, channel coding and decoding, modulation and demodulation, optical communications, satellite communications, underwater acoustic communications, radio propagation, antennas, multiuser communications, magnetic storage systems, and a variety of standards--V.1, p. v.

  State and Market in Higher Education Reforms Hans G. Schuetze,Germán Álvarez Mendiola,Diane Conrad,2012-09-05 Universities have never been static. Even so, it is fair to say they have experienced a most radical transformation in the past twenty years. During this period, the role and responsibility of the state generally have been broadly limited while allowing 'market forces'--private ownership and control--more influence. But even where the state is still the main provider or funder, it relies increasingly on 'market mechanisms', for example contractual relations between state and institutions, competition among providers for resources, and external assessment of 'outputs' which means the results or impact of what universities do, in particular teaching and research. The new terminology speaks of price and competition, inputs and outputs, resources, cost and benefits, demand and supply, provider and customer, consumers and investors, quality control and accountability. Education, and post-secondary education especially are increasingly seen as matters for markets. Formal post-secondary education becomes a service, commercialized and

traded across national borders. This volume on changing relationship between state and market, contains, besides an introductory analytic overview of the issues, accounts from different countries, regions, and thematic perspectives. Chapter authors describe and analyze government reforms and other developments that have directly or indirectly affected this relationship. Although the geographical focus is on North America, especially Mexico, South East Asia and Europe, the phenomenon is not limited to these regions and countries but worldwide.

Why Not? Barry Nalebuff,Ian Ayres,2006 A primer for fresh thinking, for problem-solving with a purpose, for bringing the world a few steps closer to the way it should be. Illustrated with examples from every aspect of life, this book offers techniques which help you take the things we all see, every day, and think about them in a new way.

Virtual, Augmented and Mixed Reality: Interaction, Navigation, Visualization, Embodiment, and Simulation Jessie Y.C. Chen,Gino Fragomeni,2018-07-10 This two-volume set LNCS 10909 and 10910 constitutes the refereed proceedings of the 10th International Conference on Virtual, Augmented and Mixed Reality, VAMR 2018, held as part of HCI International 2018 in Las Vegas, NV, USA. HCII 2018 received a total of 4346 submissions, of which 1171 papers and 160 posters were accepted for publication after a careful reviewing process. The 65 papers presented in this volume were organized in topical sections named: interaction, navigation, and visualization in VAMR; embodiment, communication, and collaboration in VAMR; education, training, and simulation; VAMR in psychotherapy, exercising, and health; virtual reality for cultural heritage, entertainment, and games; industrial and military applications.

Implementing an IBM High-Performance Computing Solution on IBM POWER8 Dino Quintero,Wei Li,Wainer dos Santos Moschetta,Mauricio Faria de Oliveira,Alexander Pozdneev,IBM Redbooks,2015-09-15 This IBM® Redbooks® publication documents and addresses topics to provide step-by-step programming concepts to tune the applications to use IBM POWER8® hardware architecture with the technical computing software stack. This publication explores, tests, and documents how to implement an IBM high-performance computing (HPC) solution on POWER8 by using IBM technical innovations to help solve challenging scientific, technical, and business problems. This book demonstrates and documents that the combination of IBM HPC hardware and software solutions delivers significant value to technical computing clients in need of cost-effective, highly scalable, and robust solutions. This book targets technical professionals (consultants, technical support staff, IT Architects, and IT Specialists) who are responsible for delivering cost-effective HPC solutions that help uncover insights among clients' data so that they can act to optimize business results, product development, and scientific discoveries.

Performance Optimization and Tuning Techniques for IBM Power Systems Processors Including IBM POWER8 Brian Hall,Peter Bergner,Alon Shalev Housfater,Madhusudanan Kandasamy,Tulio Magno,Alex Mericas,Steve Munroe,Mauricio Oliveira,Bill Schmidt,Will Schmidt,Bernard King Smith,Julian Wang,Suresh Warrier,David Wendt,IBM Redbooks,2017-03-31 This IBM® Redbooks® publication focuses on gathering the correct technical information, and laying out simple guidance for optimizing code performance on IBM POWER8® processor-based systems that run the IBM AIX®, IBM i, or Linux operating systems. There is straightforward performance optimization that can be performed with a minimum of effort and without extensive previous experience or in-depth knowledge. The POWER8 processor contains many new and important performance features, such as support for eight hardware threads in each core and support for transactional memory. The POWER8 processor is a strict superset of the IBM POWER7+TM processor, and so all of the performance features of the POWER7+ processor, such as multiple page sizes, also appear in the POWER8 processor. Much of the technical information and guidance for optimizing performance on POWER8 processors that is presented in this guide also applies to POWER7+ and earlier processors, except where the guide explicitly indicates that a feature is new in the POWER8 processor. This guide strives to focus on optimizations that tend to be positive across a broad set of IBM POWER® processor chips and systems. Specific guidance is given for the POWER8 processor; however, the general guidance is applicable to the IBM POWER7+, IBM POWER7®, IBM POWER6®, IBM POWER5, and even to earlier processors. This guide is directed at personnel who are responsible for performing migration and implementation activities on POWER8 processor-based systems. This includes system administrators, system architects, network administrators, information architects, and database administrators (DBAs).

Oracle Real Application Clusters Murali Vallath,2004 Practical guide to RAC architecture for data base managers to manage Oracle9i clusters.

Transactional Memory. Foundations, Algorithms, Tools, and Applications Rachid Guerraoui,Paolo Romano,2014-12-29 The advent of multi-core architectures and cloud-computing has brought parallel programming into the mainstream of software development. Unfortunately, writing scalable parallel programs using traditional lock-based synchronization primitives is well known to be a hard, time consuming and error-prone task, mastered by only a minority of specialized programmers. Building on the familiar abstraction of atomic transactions, Transactional Memory (TM) promises to free programmers from the complexity of conventional synchronization schemes, simplifying the development and verification of concurrent programs, enhancing code reliability, and boosting productivity. Over the last decade TM has been subject to intense research on a broad range of aspects including hardware and operating systems support, language integration, as well as algorithms and theoretical foundations. On the industrial side, the major players of the software and hardware markets have been up-front in the research and development of prototypal products providing support for TM systems. This has recently led to the introduction of hardware TM implementations on mainstream commercial microprocessors and to the integration of TM support for the world's leading open source compiler. In such a vast inter-disciplinary domain, the Euro-TM COST Action (IC1001) has served as a catalyzer and a bridge for the various research communities looking at disparate, yet subtly interconnected, aspects of TM. This book emerged from the idea having Euro-TM experts compile recent results in the TM area in a single and consistent volume. Contributions have been carefully selected and revised to provide a broad coverage of several fundamental issues associated with the design and implementation of TM systems, including their theoretical underpinnings and algorithmic foundations, programming language integration and verification tools, hardware supports, distributed TM systems, self-tuning mechanisms, as well as lessons learnt from building complex TM-based applications.

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