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Colin Fyfe, Peter Tino, Darryl Charles, Cesar Garcia Osorio, Hujun Yin

*Oracle Data Integrator 11g Cookbook* Christophe Dupupet, 2013-05-15 Written as a practical Cookbook, the recipes in this essential guide will help you make the most out of Oracle Data Integrator 11g. This book is meant for people who already possess a basic understanding of Oracle Data Integrator and want to take it to the next level by learning how to better leverage advanced ODI features and functionality as they continue to develop and manage their data integration projects.

OpenMP: Heterogenous Execution and Data Movements Christian Terboven, Bronis R. de Supinski, Pablo Reble, Barbara M. Chapman, Matthias S. Müller, 2015-09-30 This book constitutes the refereed proceedings of the 11th International Workshop on OpenMP, held in Aachen, Germany, in October 2015. The 19 technical full papers presented were carefully reviewed and selected from 22 submissions. The papers are organized in topical sections on applications, accelerator applications, tools, extensions, compiler and runtime, and energy.

**Architecture of Computing Systems -- ARCS 2013** Hana Kubatova, Christian Hochberger, Martin Daněš, Bernhard Sick, 2013-02-12 This book constitutes the refereed proceedings of the 26th International Conference on Architecture of Computing Systems, ARCS 2013, held in Prague, Czech Republic, in February 2013. The 29 papers presented were carefully reviewed and selected from 73 submissions. The topics covered are computer architecture topics such as multi-cores, memory systems, and parallel computing, adaptive system architectures such as reconfigurable systems in hardware and software, customization and application specific accelerators in heterogeneous architectures, organic and autonomic computing including both theoretical and practical results on self-organization, self-configuration, self-optimization, self-healing, and self-protection techniques, operating systems including but not limited to scheduling, memory management, power management, RTOS, energy-awareness, and green computing.

*Intelligent Data Engineering and Automated Learning -- IDEAL 2010* Colin Fyfe, Peter Tino, Darryl Charles, Cesar Garcia Osorio, Hujun Yin, 2010-08-21 The IDEAL conference has become a unique, established and broad interdisciplinary forum for experts, researchers and practitioners in many fields to interact with each other and with leading academics and industries in the areas of machine learning, information processing, data mining, knowledge management, bio-informatics, neu- informatics, bio-inspired models, agents and distributed systems, and hybrid systems. This volume contains the papers presented at the 11th International Conference on Intelligent Data Engineering and Automated Learning (IDEAL 2010), which was held September 1–3, 2010 in the University of the West of Scotland, on its Paisley campus, 15 kilometres from the city of Glasgow, Scotland. All submissions were strictly peer-reviewed by the Programme Committee and only the papers judged with sufficient quality and novelty were accepted and included in the proceedings. The IDEAL conferences continue to evolve and this year's conference was no exception. The conference papers cover a wide variety of topics which can be classified by technique, aim or application. The techniques include evolutionary algorithms, artificial neural networks, association rules, probabilistic modelling, agent modelling, particle swarm optimization and kernel methods. The aims include

regression, classification, clustering and generic data mining. The applications include biological information processing, text processing, physical systems control, video analysis and time series analysis.

*Transactional Memory, Second Edition* Tim Harris,James Larus,Ravi Rajwar,2022-05-31 The advent of multicore processors has renewed interest in the idea of incorporating transactions into the programming model used to write parallel programs. This approach, known as transactional memory, offers an alternative, and hopefully better, way to coordinate concurrent threads. The ACI (atomicity, consistency, isolation) properties of transactions provide a foundation to ensure that concurrent reads and writes of shared data do not produce inconsistent or incorrect results. At a higher level, a computation wrapped in a transaction executes atomically - either it completes successfully and commits its result in its entirety or it aborts. In addition, isolation ensures the transaction produces the same result as if no other transactions were executing concurrently. Although transactions are not a parallel programming panacea, they shift much of the burden of synchronizing and coordinating parallel computations from a programmer to a compiler, to a language runtime system, or to hardware. The challenge for the system implementers is to build an efficient transactional memory infrastructure. This book presents an overview of the state of the art in the design and implementation of transactional memory systems, as of early spring 2010. Table of Contents: Introduction / Basic Transactions / Building on Basic Transactions / Software Transactional Memory / Hardware-Supported Transactional Memory / Conclusions

**Transactional Memory, 2nd Edition** Tim Harris,James Larus,Ravi Rajwar,2010-10-10 The advent of multicore processors has renewed interest in the idea of incorporating transactions into the programming model used to write parallel programs. This approach, known as transactional memory, offers an alternative, and hopefully better, way to coordinate concurrent threads. The ACI (atomicity, consistency, isolation) properties of transactions provide a foundation to ensure that concurrent reads and writes of shared data do not produce inconsistent or incorrect results. At a higher level, a computation wrapped in a transaction executes atomically - either it completes successfully and commits its result in its entirety or it aborts. In addition, isolation ensures the transaction produces the same result as if no other transactions were executing concurrently. Although transactions are not a parallel programming panacea, they shift much of the burden of synchronizing and coordinating parallel computations from a programmer to a compiler, to a language runtime system, or to hardware. The challenge for the system implementers is to build an efficient transactional memory infrastructure. This book presents an overview of the state of the art in the design and implementation of transactional memory systems, as of early spring 2010. Table of Contents: Introduction / Basic Transactions / Building on Basic Transactions / Software Transactional Memory / Hardware-Supported Transactional Memory / Conclusions

*Laboratory Information Bulletin* ,1998

**United States Demographics - Part B** ,

**Anomaly Detection and Complex Event Processing Over IoT Data Streams** Patrick Schneider,Fatos Xhafa,2022-01-07  
Anomaly Detection and Complex Event Processing over IoT Data Streams: With Application to eHealth and Patient Data

Monitoring presents advanced processing techniques for IoT data streams and the anomaly detection algorithms over them. The book brings new advances and generalized techniques for processing IoT data streams, semantic data enrichment with contextual information at Edge, Fog and Cloud as well as complex event processing in IoT applications. The book comprises fundamental models, concepts and algorithms, architectures and technological solutions as well as their application to eHealth. Case studies, such as the bio-metric signals stream processing are presented –the massive amount of raw ECG signals from the sensors are processed dynamically across the data pipeline and classified with modern machine learning approaches including the Hierarchical Temporal Memory and Deep Learning algorithms. The book discusses adaptive solutions to IoT stream processing that can be extended to different use cases from different fields of eHealth, to enable a complex analysis of patient data in a historical, predictive and even prescriptive application scenarios. The book ends with a discussion on ethics, emerging research trends, issues and challenges of IoT data stream processing. Provides the state-of-the-art in IoT Data Stream Processing, Semantic Data Enrichment, Reasoning and Knowledge Covers extraction (Anomaly Detection) Illustrates new, scalable and reliable processing techniques based on IoT stream technologies Offers applications to new, real-time anomaly detection scenarios in the health domain

*Official Gazette of the United States Patent and Trademark Office* United States. Patent and Trademark Office,2001

**Mastering Delphi 6** Marco Cantù,2006-02-20 Still the Best Delphi Resource A Fully Revised Version of the Book That Won the DelphiInformant Readers Choice Award If you're looking to capitalize on the powerful capabilities ofthe latest release of Delphi, Mastering Delphi 6 is the oneresource you can't do without. Practical, tutorial-based coveragehelps you develop key skills, solve tough problems, and build andimplement sophisticated functionality in your database,client/server, and Internet applications. The special insights ofDelphi expert Marco Cantu you an extra advantage as you completesuccessful projects and progress towards Delphi mastery. Coverage includes: Getting to know the new run-time library (RTL) Learning to use the VCL and CLX visual libraries Developing custom components Creating data-aware controls and custom datasets Mastering database programming with BDE and dbExpress Mastering client/server programming with InterBase Interfacing with Microsoft's ADO using the dbGo componentset Taking advantage of Delphi's support for COM, OLE Automation,and COM+ Programming for a multitiered application architecture Taking advantage of Delphi's support for XML and SOAPtechnologies Building practical, powerful Web applications Note: CD-ROM/DVD and other supplementary materials arenot included as part of eBook file.

**Troubled Ground** Claude A. Clegg,2010-11-05 Cover -- Title Page -- Copyright -- Table of Contents -- Acknowledgments -- Prologue: Searching for a Troubled Past -- 1. Bygones -- 2. Old Demons of the New South -- 3. The Reaping -- 4. Presumed Guilt -- 5. A Blot Upon the State -- 6. A Reckoning -- Epilogue -- Appendix -- Notes -- Bibliography -- Index -- Cover 4.

**ASP.NET for Developers** Michael Amundsen,Paul Litwin,2002 Current developers value their time. For many,

consulting rates start at 35 to 50 an hour. Their time is very valuable, thus reading an 800 page book becomes very expensive. At conferences such as Microsoft's PDC and Tech Ed, these developers often are asking for short, concise books full of information. ASP.NET For Developers just that a concise book that focuses on teaching the reader ASP.NET using Visual Basic.NET. The book starts by presenting the key concepts with using ASP.NET and Visual Basic.NET. Because the reader is assumed to be an experienced developer, this information is presente.

SAS Data Analytic Development Troy Martin Hughes, 2016-08-24 Design quality SAS software and evaluate SAS software quality SAS Data Analytic Development is the developer's compendium for writing better-performing software and the manager's guide to building comprehensive software performance requirements. The text introduces and parallels the International Organization for Standardization (ISO) software product quality model, demonstrating 15 performance requirements that represent dimensions of software quality, including: reliability, recoverability, robustness, execution efficiency (i.e., speed), efficiency, scalability, portability, security, automation, maintainability, modularity, readability, testability, stability, and reusability. The text is intended to be read cover-to-cover or used as a reference tool to instruct, inspire, deliver, and evaluate software quality. A common fault in many software development environments is a focus on functional requirements—the what and how—to the detriment of performance requirements, which specify instead how well software should function (assessed through software execution) or how easily software should be maintained (assessed through code inspection). Without the definition and communication of performance requirements, developers risk either building software that lacks intended quality or wasting time delivering software that exceeds performance objectives—thus, either underperforming or gold-plating, both of which are undesirable. Managers, customers, and other decision makers should also understand the dimensions of software quality both to define performance requirements at project outset as well as to evaluate whether those objectives were met at software completion. As data analytic software, SAS transforms data into information and ultimately knowledge and data-driven decisions. Not surprisingly, data quality is a central focus and theme of SAS literature; however, code quality is far less commonly described and too often references only the speed or efficiency with which software should execute, omitting other critical dimensions of software quality. SAS® software project definitions and technical requirements often fall victim to this paradox, in which rigorous quality requirements exist for data and data products yet not for the software that undergirds them. By demonstrating the cost and benefits of software quality inclusion and the risk of software quality exclusion, stakeholders learn to value, prioritize, implement, and evaluate dimensions of software quality within risk management and project management frameworks of the software development life cycle (SDLC). Thus, SAS Data Analytic Development recalibrates business value, placing code quality on par with data quality, and performance requirements on par with functional requirements.

**Mastering Borland Delphi 2005** Marco Cant?, 2005-08-19 If you're looking to capitalize on the powerful capabilities of Delphi 8, this is your essential resource. Named Best Book by Delphi Informant magazine, its practical, tutorial-based coverage helps you develop key skills, solve tough problems, and build functionality in

your applications.

**Encyclopedia of Parallel Computing** David Padua,2014-07-08 Containing over 300 entries in an A-Z format, the Encyclopedia of Parallel Computing provides easy, intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing. Topics for this comprehensive reference were selected, written, and peer-reviewed by an international pool of distinguished researchers in the field. The Encyclopedia is broad in scope, covering machine organization, programming languages, algorithms, and applications. Within each area, concepts, designs, and specific implementations are presented. The highly-structured essays in this work comprise synonyms, a definition and discussion of the topic, bibliographies, and links to related literature. Extensive cross-references to other entries within the Encyclopedia support efficient, user-friendly searches for immediate access to useful information. Key concepts presented in the Encyclopedia of Parallel Computing include; laws and metrics; specific numerical and non-numerical algorithms; asynchronous algorithms; libraries of subroutines; benchmark suites; applications; sequential consistency and cache coherency; machine classes such as clusters, shared-memory multiprocessors, special-purpose machines and dataflow machines; specific machines such as Cray supercomputers, IBM's cell processor and Intel's multicore machines; race detection and auto parallelization; parallel programming languages, synchronization primitives, collective operations, message passing libraries, checkpointing, and operating systems. Topics covered: Speedup, Efficiency, Isoefficiency, Redundancy, Amdahls law, Computer Architecture Concepts, Parallel Machine Designs, Benmarks, Parallel Programming concepts & design, Algorithms, Parallel applications. This authoritative reference will be published in two formats: print and online. The online edition features hyperlinks to cross-references and to additional significant research. Related Subjects: supercomputing, high-performance computing, distributed computing

*Distributed Computing* Nancy A. Lynch,Alexander A. Shvartsman,2010-08-24 This book constitutes the refereed proceedings of the 24th International Symposium on Distributed Computing, DISC 2010, held in Cambridge, CT, USA, in September 2010. The 32 revised full papers, selected from 135 submissions, are presented together with 14 brief announcements of ongoing works; all of them were carefully reviewed and selected for inclusion in the book. The papers address all aspects of distributed computing, and were organized in topical sections on, transactions, shared memory services and concurrency, wireless networks, best student paper, consensus and leader election, mobile agents, computing in wireless and mobile networks, modeling issues and adversity, and self-stabilizing and graph algorithms.

Finding Out Deborah T. Meem,Jonathan Alexander,Key Beck,Michelle A. Gibson,2022-08-02 Finding Out, Fourth Edition introduces readers to lesbian-gay-bisexual-transgender-queer (LGBTQ) studies. By combining accessible introductory and explanatory material with primary texts and artifacts, this text/reader explores the development and growth of LGBTQ identities and the interdisciplinary nature of sexuality studies. Now available in a digital ebook format, the fourth edition has been thoroughly updated to include a new chapter on Trans Lives and Theories,

and new readings. Authors Deborah T. Meem, Jonathan Alexander, Key Beck, and Michelle A. Gibson provide more discussions of important and current issues in LGBTQ studies such as the emergence of non-binary identities, and issues of race and class, making Finding Out, Fourth Edition an even more comprehensive introduction to the field.

**Transactional Memory. Foundations, Algorithms, Tools, and Applications** Rachid Guerraoui, Paolo Romano, 2014-12-29  
The advent of multi-core architectures and cloud-computing has brought parallel programming into the mainstream of software development. Unfortunately, writing scalable parallel programs using traditional lock-based synchronization primitives is well known to be a hard, time consuming and error-prone task, mastered by only a minority of specialized programmers. Building on the familiar abstraction of atomic transactions, Transactional Memory (TM) promises to free programmers from the complexity of conventional synchronization schemes, simplifying the development and verification of concurrent programs, enhancing code reliability, and boosting productivity. Over the last decade TM has been subject to intense research on a broad range of aspects including hardware and operating systems support, language integration, as well as algorithms and theoretical foundations. On the industrial side, the major players of the software and hardware markets have been up-front in the research and development of prototypal products providing support for TM systems. This has recently led to the introduction of hardware TM implementations on mainstream commercial microprocessors and to the integration of TM support for the world's leading open source compiler. In such a vast inter-disciplinary domain, the Euro-TM COST Action (IC1001) has served as a catalyzer and a bridge for the various research communities looking at disparate, yet subtly interconnected, aspects of TM. This book emerged from the idea having Euro-TM experts compile recent results in the TM area in a single and consistent volume. Contributions have been carefully selected and revised to provide a broad coverage of several fundamental issues associated with the design and implementation of TM systems, including their theoretical underpinnings and algorithmic foundations, programming language integration and verification tools, hardware supports, distributed TM systems, self-tuning mechanisms, as well as lessons learnt from building complex TM-based applications.

*Criminal Procedure for the Criminal Justice Professional* John N. Ferdico, 2005

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