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Jonathan W. Valvano

PoC or GTFO Manul Laphroaig,2017-10-31 This highly anticipated print collection gathers articles published in the much-loved International Journal of Proof-of-Concept or Get The Fuck Out. PoC||GTFO follows in the tradition of Phrack and Uninformed by publishing on the subjects of offensive security research, reverse engineering, and file format internals. Until now, the journal has only been available online or printed and distributed for free at hacker conferences worldwide. Consistent with the journal's quirky, biblical style, this book comes with all the trimmings: a leatherette cover, ribbon bookmark, bible paper, and gilt-edged pages. The book features more than 80 technical essays from numerous famous hackers, authors of classics like Reliable Code Execution on a Tamagotchi, ELFs are Dorky, Elves are Cool, Burning a Phone, Forget Not the Humble Timing Attack, and A Sermon on Hacker Privilege. Twenty-four full-color pages by Ange Albertini illustrate many of the clever tricks described in the text.

Modern X86 Assembly Language Programming Daniel Kusswurm,2014-11-29 Modern X86 Assembly Language Programming shows the fundamentals of x86 assembly language programming. It focuses on the aspects of the x86 instruction set that are most relevant to application software development. The book's structure and sample code are designed to help the reader quickly understand x86 assembly language programming and the computational capabilities of the x86 platform. Please note: Book appendixes can be downloaded here: http://www.apress.com/9781484200650 Major topics of the book include the following: 32-bit core architecture, data types, internal registers, memory addressing modes, and the basic instruction set X87 core architecture, register stack, special purpose

registers, floating-point encodings, and instruction set MMX technology and instruction set Streaming SIMD extensions (SSE) and Advanced Vector Extensions (AVX) including internal registers, packed integer arithmetic, packed and scalar floating-point arithmetic, and associated instruction sets 64-bit core architecture, data types, internal registers, memory addressing modes, and the basic instruction set 64-bit extensions to SSE and AVX technologies X86 assembly language optimization strategies and techniques

Mobile Unleashed Don Dingee,2015-12-08 This is the origin story of technology super heroes: the creators and founders of ARM, the company that is responsible for the processors found inside 95% of the world's mobile devices today. This is also the evolution story of how three companies - Apple, Samsung, and Qualcomm - put ARM technology in the hands of billions of people through smartphones, tablets, music players, and more. It was anything but a straight line from idea to success for ARM. The story starts with the triumph of BBC Micro engineers Steve Furber and Sophie Wilson, who make the audacious decision to design their own microprocessor - and it works the first time. The question becomes, how to sell it? Part I follows ARM as its founders launch their own company, select a new leader, a new strategy, and find themselves partnered with Apple, TI, Nokia, and other companies just as digital technology starts to unleash mobile devices. ARM grows rapidly, even as other semiconductor firms struggle in the dot com meltdown, and establishes itself as a standard for embedded RISC processors. Apple aficionados will find the opening of Part II of interest the moment Steve Jobs returns and changes the direction toward fulfilling consumer dreams. Samsung devotees will see how that firm evolved from its earliest days in consumer electronics and semiconductors through a philosophical shift

to innovation. Qualcomm followers will learn much of their history as it plays out from satellite communications to development of a mobile phone standard and emergence as a leading fabless semiconductor company. If ARM could be summarized in one word, it would be collaboration. Throughout this story, from Foreword to Epilogue, efforts to develop an ecosystem are highlighted. Familiar names such as Google, Intel, Mediatek, Microsoft, Motorola, TSMC, and others are interwoven throughout. The evolution of ARM's first 25 years as a company wraps up with a shift to its next strategy: the Internet of Things, the ultimate connector for people and devices. Research for this story is extensive, simplifying a complex mobile industry timeline and uncovering critical points where ARM and other companies made fateful and sometimes surprising decisions. Rare photos, summary diagrams and tables, and unique perspectives from insiders add insight to this important telling of technology history.

Robot Operating System (ROS) Anis Koubaa,2016-02-09 The objective of this book is to provide the reader with a comprehensive coverage on the Robot Operating Systems (ROS) and latest related systems, which is currently considered as the main development framework for robotics applications. The book includes twenty-seven chapters organized into eight parts. Part 1 presents the basics and foundations of ROS. In Part 2, four chapters deal with navigation, motion and planning. Part 3 provides four examples of service and experimental robots. Part 4 deals with real-world deployment of applications. Part 5 presents signal-processing tools for perception and sensing. Part 6 provides software engineering methodologies to design complex software with ROS. Simulations frameworks are presented in Part 7. Finally, Part 8 presents advanced tools and frameworks for ROS

including multi-master extension, network introspection, controllers and cognitive systems. This book will be a valuable companion for ROS users and developers to learn more ROS capabilities and features.

Developments and Advances in Defense and Security Álvaro Rocha,Robson Pacheco Pereira,2019-06-13 This book gathers the proceedings of the Multidisciplinary International Conference of Research Applied to Defense and Security (MICRADS), held at the Military Engineering Institute, Rio de Janeiro, Brazil, from 8 to 10th May 2019. It covers a variety of topics in systems, communication and defense; strategy and political-administrative vision in defense; and engineering and technologies applied to defense. Given its scope, it offers a valuable resource for practitioners, researchers, and students alike.

Real-Time Bluetooth Networks Jonathan W. Valvano,2016-11-14 Welcome to Real-Time Bluetooth Networks - Shape the World. This book, now in its second printing December 2017, offers a format geared towards hands-on self-paced learning. The overarching goal is to give you the student an experience with real-time operating systems that is based on the design and development of a simplified RTOS that exercises all the fundamental concepts. To keep the discourse grounded in practice we have refrained from going too deep into any one topic. We believe this will equip the student with the knowledge necessary to explore more advanced topics on their own. In essence, we will teach you the skills of the trade, but mastery is the journey you will have to undertake on your own. An operating system (OS) is layer of software that sits on top of the hardware. It manages the hardware resources so that the applications have the illusion that they own the hardware all to themselves. A real-time system is one that not only gets the correct

answer but gets the correct answer at the correct time. Design and development of an OS therefore requires both, understanding the underlying architecture in terms of the interface (instruction set architecture, ISA) it provides to the software, and organizing the software to exploit this interface and present it to user applications. The decisions made in effectively managing the underlying architecture becomes more crucial in real-time systems as the performance (specifically timing) demands go beyond simple logical correctness. The architecture we will focus on is the ARM ISA, which is a very popular architecture in the embedded device ecosystem where real-time systems proliferate. A quick introduction to the ISA will be followed by specifics of TI's offering of this ISA as the Tiva and MSP432 Launchpad microcontroller. To make the development truly compelling we need a target application that has real-time constraints and multi-threading needs. To that end you will incrementally build a personal fitness device with Bluetooth connectivity. The Bluetooth connectivity will expose you to the evolving domain of Internet-of-things (IoT) where our personal fitness device running a custom RTOS will interact with a smartphone.

Computational Intelligence in Pattern Recognition Asit Kumar Das,Janmenjoy Nayak,Bighnaraj Naik,Soumi Dutta,Danilo Pelusi,2020-02-19 This book features high-quality research papers presented at the 2nd International Conference on Computational Intelligence in Pattern Recognition (CIPR 2020), held at the Institute of Engineering and Management, Kolkata, West Bengal, India, on 4–5 January 2020. It includes practical development experiences in various areas of data analysis and pattern recognition, focusing on soft computing technologies, clustering and classification algorithms, rough set and fuzzy set theory, evolutionary computations, neural science and neural network

systems, image processing, combinatorial pattern matching, social network analysis, audio and video data analysis, data mining in dynamic environments, bioinformatics, hybrid computing, big data analytics and deep learning. It also provides innovative solutions to the challenges in these areas and discusses recent developments.

The Ghidra Book Chris Eagle,Kara Nance,2020-09-08 A guide to using the Ghidra software reverse engineering tool suite. The result of more than a decade of research and development within the NSA, the Ghidra platform was developed to address some of the agency's most challenging reverse-engineering problems. With the open-source release of this formerly restricted tool suite, one of the world's most capable disassemblers and intuitive decompilers is now in the hands of cybersecurity defenders everywhere -- and The Ghidra Book is the one and only guide you need to master it. In addition to discussing RE techniques useful in analyzing software and malware of all kinds, the book thoroughly introduces Ghidra's components, features, and unique capacity for group collaboration. You'll learn how to: Navigate a disassembly Use Ghidra's built-in decompiler to expedite analysis Analyze obfuscated binaries Extend Ghidra to recognize new data types Build new Ghidra analyzers and loaders Add support for new processors and instruction sets Script Ghidra tasks to automate workflows Set up and use a collaborative reverse engineering environment Designed for beginner and advanced users alike, The Ghidra Book will effectively prepare you to meet the needs and challenges of RE, so you can analyze files like a pro.

Advances in VLSI, Signal Processing, Power Electronics, IoT, Communication and Embedded Systems Shubhakar Kalya,Muralidhar Kulkarni,K. S.

Shivaprakasha,2021-04-10 This book comprises select peer-reviewed papers from the International Conference on VLSI, Signal Processing, Power Electronics, IoT, Communication and Embedded Systems (VSPICE-2020). The book provides insights into various aspects of the emerging fields in the areas Electronics and Communication Engineering as a holistic approach. The various topics covered in this book include VLSI, embedded systems, signal processing, communication, power electronics and internet of things. This book mainly focuses on the most recent innovations, trends, concerns and practical challenges and their solutions. This book will be useful for academicians, professionals and researchers in the area of electronics and communications and electrical engineering.

The FreeBSD Handbook Walnut Creek CD-ROM,2000-05-31 The FreeBSD Handbook is a comprehensive FreeBSD tutorial and reference. It covers installation, day-to-day use of FreeBSD, Ports collection, creating a custom kernel, security topics, the X Window System, how to use FreeBSD's Linux binary compatibility, and how to upgrade your system from source using the make world command.

Code Charles Petzold,2022-08-02 The classic guide to how computers work, updated with new chapters and interactive graphics For me, Code was a revelation. It was the first book about programming that spoke to me. It started with a story, and it built up, layer by layer, analogy by analogy, until I understood not just the Code, but the System. Code is a book that is as much about Systems Thinking and abstractions as it is about code and programming. Code teaches us how many unseen layers there are between the computer systems that we as users look at every day and the magical silicon rocks that we infused with lightning and taught to think. - Scott Hanselman, Partner Program Director, Microsoft,

and host of Hanselminutes Computers are everywhere, most obviously in our laptops and smartphones, but also our cars, televisions, microwave ovens, alarm clocks, robot vacuum cleaners, and other smart appliances. Have you ever wondered what goes on inside these devices to make our lives easier but occasionally more infuriating? For more than 20 years, readers have delighted in Charles Petzold's illuminating story of the secret inner life of computers, and now he has revised it for this new age of computing. Cleverly illustrated and easy to understand, this is the book that cracks the mystery. You'll discover what flashlights, black cats, seesaws, and the ride of Paul Revere can teach you about computing, and how human ingenuity and our compulsion to communicate have shaped every electronic device we use. This new expanded edition explores more deeply the bit-by-bit and gate-by-gate construction of the heart of every smart device, the central processing unit that combines the simplest of basic operations to perform the most complex of feats. Petzold's companion website, CodeHiddenLanguage.com, uses animated graphics of key circuits in the book to make computers even easier to comprehend. In addition to substantially revised and updated content, new chapters include: Chapter 18: Let's Build a Clock! Chapter 21: The Arithmetic Logic Unit Chapter 22: Registers and Busses Chapter 23: CPU Control Signals Chapter 24: Jumps, Loops, and Calls Chapter 28: The World Brain From the simple ticking of clocks to the worldwide hum of the internet, Code reveals the essence of the digital revolution.

    Advances in Computer Games Mark H.M. Winands,H. Jaap van den Herik,Walter A. Kosters,2017-12-21 This book constitutes the refereed conference proceedings of the 15th International Conference, ACG 2017, held in Leiden, The Netherlands, in July 2017.The 19

revised full papers were selected from 23 submissions and cover a wide range of computer games. They are grouped in four classes according to the order of publication: games and puzzles, go and chess, machine learning and MCTS, and gaming.

Mastering OpenCV with Practical Computer Vision Projects Daniel Lélis Baggio,2012-12-03 Each chapter in the book is an individual project and each project is constructed with step-by-step instructions, clearly explained code, and includes the necessary screenshots. You should have basic OpenCV and C/C++ programming experience before reading this book, as it is aimed at Computer Science graduates, researchers, and computer vision experts widening their expertise.

IBM Power System AC922 Introduction and Technical Overview Scott Vetter,Alexandre Bicas Caldeira,IBM Redbooks,2018-03-26 This IBM® RedpaperTM publication is a comprehensive guide that covers the IBM Power System AC922 server (8335-GTG and 8335-GTW models). The Power AC922 server is the next generation of the IBM Power processor-based systems, which are designed for deep learning and artificial intelligence (AI), high-performance analytics, and high-performance computing (HPC). This paper introduces the major innovative Power AC922 server features and their relevant functions: Powerful IBM POWER9TM processors that offer 16 cores at 2.6 GHz with 3.09 GHz turbo performance or 20 cores at 2.0 GHz with 2.87 GHz turbo for the 8335-GTG Eighteen cores at 2.98 GHz with 3.26 GHz turbo performance or 22 at 2.78 GHz cores with 3.07 GHz turbo for the 8335-GTW IBM Coherent Accelerator Processor Interface (CAPI) 2.0, IBM OpenCAPITM, and second-generation NVIDIA NVLink technology for exceptional processor-to-accelerator intercommunication Up to six dedicated NVIDIA Tesla V100 GPUs This

publication is for professionals who want to acquire a better understanding of IBM Power SystemsTM products and is intended for the following audiences: Clients Sales and marketing professionals Technical support professionals IBM Business Partners Independent software vendors (ISVs) This paper expands the set of IBM Power Systems documentation by providing a desktop reference that offers a detailed technical description of the Power AC922 server. This paper does not replace the current marketing materials and configuration tools. It is intended as an extra source of information that, together with existing sources, can be used to enhance your knowledge of IBM server solutions.

Digital Information Processing and Communications Václav Snásel,Jan Platos,Eyas El-Qawasmeh,2011-06-30 This two-volume-set (CCIS 188 and CCIS 189) constitutes the refereed proceedings of the International Conference on Digital Information Processing and Communications, ICDIPC 2011, held in Ostrava, Czech Republic, in July 2011. The 91 revised full papers of both volumes presented together with 4 invited talks were carefully reviewed and selected from 235 submissions. The papers are organized in topical sections on network security; Web applications; data mining; neural networks; distributed and parallel processing; biometrics technologies; e-learning; information ethics; image processing; information and data management; software engineering; data compression; networks; computer security; hardware and systems; multimedia; ad hoc network; artificial intelligence; signal processing; cloud computing; forensics; security; software and systems; mobile networking; and some miscellaneous topics in digital information and communications.

Model-Based Systems Engineering A. Wayne Wymore,2018-05-04 Model-Based Systems

Engineering explains the fundamental theories behind model-based systems and the considerations involved in applying theory to the design of real systems. The book begins by presenting terms used in systems engineering and introducing the discrete system and its components. The remainder of the text explains topics such as the mathematical theory of system coupling, the homomorphic relationship between systems, the concept of system mode, the mathematical structure of T3SD system requirements, and the implications of that structure for T3SD system design. Appendices include a short bibliography, detailed definitions of all examples discussed in the text, a list of all notations used, and an index. Model-Based Systems Engineering is an excellent text for engineering students, and an invaluable reference for engineers and scientists.

Implementing an IBM High-Performance Computing Solution on IBM Power System S822LC Dino Quintero,Luis Carlos Cruz Huertas,Tsuyoshi Kamenoue,Wainer dos Santos Moschetta,Mauricio Faria de Oliveira,Georgy E Pavlov,Alexander Pozdneev,IBM Redbooks,2016-07-25 This IBM® Redbooks® publication demonstrates and documents that IBM Power SystemsTM high-performance computing and technical computing solutions deliver faster time to value with powerful solutions. Configurable into highly scalable Linux clusters, Power Systems offer extreme performance for demanding workloads such as genomics, finance, computational chemistry, oil and gas exploration, and high-performance data analytics. This book delivers a high-performance computing solution implemented on the IBM Power System S822LC. The solution delivers high application performance and throughput based on its built-for-big-data architecture that incorporates IBM POWER8® processors, tightly coupled Field Programmable Gate Arrays (FPGAs) and accelerators, and

faster I/O by using Coherent Accelerator Processor Interface (CAPI). This solution is ideal for clients that need more processing power while simultaneously increasing workload density and reducing datacenter floor space requirements. The Power S822LC offers a modular design to scale from a single rack to hundreds, simplicity of ordering, and a strong innovation roadmap for graphics processing units (GPUs). This publication is targeted toward technical professionals (consultants, technical support staff, IT Architects, and IT Specialists) responsible for delivering cost effective high-performance computing (HPC) solutions that help uncover insights from their data so they can optimize business results, product development, and scientific discoveries

Retronics Jan Buiting,2013-04-01

PoC or GTFO, Volume 3 Manul Laphroaig,2021-01-29 Volume 3 of the PoC || GTFO collection--read as Proof of Concept or Get the Fuck Out--continues the series of wildly popular collections of this hacker journal. Contributions range from humorous poems to deeply technical essays bound in the form of a bible. The International Journal of Proof-of-Concept or Get The Fuck Out is a celebrated collection of short essays on computer security, reverse engineering and retrocomputing topics by many of the world's most famous hackers. This third volume contains all articles from releases 14 to 18 in the form of an actual, bound bible. Topics include how to dump the ROM from one of the most secure Sega Genesis games ever created; how to create a PDF that is also a Git repository; how to extract the Game Boy Advance BIOS ROM; how to sniff Bluetooth Low Energy communications with the BCC Micro:Bit; how to conceal ZIP Files in NES Cartridges; how to remotely exploit a TetriNET Server; and more. The journal exists to remind us of what a

clever engineer can build from a box of parts and a bit of free time. Not to showcase what others have done, but to explain how they did it so that readers can do these and other clever things themselves.

AI Superpowers Kai-Fu Lee,2018-09-25 Introduction -- China's Sputnik moment -- Copycats in the Coliseum -- China's alternate Internet universe -- A tale of two countries -- The four waves of AI -- Utopia, dystopia, and the real AI crisis -- The wisdom of cancer -- A blueprint for human co-existence with AI -- Our global AI story

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